PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants:

L. James Hwang et al.

Assignee:

Xilinx, Inc.

Title:

Method and System for Resource Allocation in FPGA-Based

System-On-Chip (SoC)

Serial No.:

10/082,441

File Date:

2-22-02

Examiner:

Stacy Whitmore

Art Unit:

2825

Docket No.:

X-1000 US

Conf. No.:

6299

COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, Virginia 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the one (1) reference listed in the attached Substitute for Form 1449A/PTO.

This Supplemental Information Disclosure Statement is being filed under 37 CFR 1.97(b) after the receipt of a first office action but prior to a final office action.

Citation of the above document shall not be construed as an admission that the document are necessarily prior art with respect to the instant invention. Citation of the above document shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above document shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

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Justin Liu

Attorney for Applicants

Respectfully submitted,

Reg. No. 51,959

I hereby certify that this correspondence is being deposited with the United States Postal Service as **first class mail** in an envelope addressed to P.O. Box 1450, Alexandria, Virginia 22313-1450 on: December 27, 2005.

Pat Tompkins

Name

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PTO/SB/08A (10-01)
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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 1

Complete if Known				
Application / Conf. No.	10/082,441 / 6299			
Filing Date	February 22, 2002			
First Named Inventor	L. James Hwang			
Art Unit	2825			
Examiner Name	Stacy Whitmore			
Attorney Docket Number	X-1000 US			

U.S. PATENT DOCUMENTS						
Examiner	Cite	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or	Pages, Columns, Lines, Where	
Initials No.		Number - Kind Code (if known)		Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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FOREIGN PATENT DOCUMENTS							
Cite			Name of Patentee or	Pages, Columns, Lines, Where Relevant Passages	7 6		
No.1	Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	MM-DD-YYYY	Applicant of Cited Document	or Relevant Figures Appear			
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	Cite No.¹	Cite Foreign Patent Document	Foreign Patent Document Publication Date	Foreign Patent Document Publication Date Name of Patentee or	Foreign Patent Document Publication Date Name of Patentee or Pages, Columns, Lines, Where Relevant Passages		

Examiner Signature	Date Considered	

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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ³ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.